

Please type a plus sign (+) inside this box

+

30

#30
PTO/SB/08B (08-00)

+

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete if Known	
			Application Number	09/057,861
			Filing Date	April 9, 1998
			First Named Inventor	Sachs, Howard
			Group Art Unit	2758 2154
			Examiner Name	V. Vu
Sheet 3 of	Attorney Docket Number	012172-004530US		

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
V.V.	AA	5,355,460		Eickemeyer et al.	10/11/94	
V.V.	AB	5,459,844		Eickemeyer et al.	10/17/95	

FOREIGN PATENT DOCUMENTS								
Examiner Initials *	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
V.V.	AC	EP	0463296	A2	Blaner	01/02/1992		

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
V.V.	AD	Horst et al., "Multiple instruction issue in the nonstop cyclone processor" Proceedings of the 17th Annual International Symposium on Computer Architecture, IEEE Computer Society Press, Washington, (May 28-31, 1990) pp. 216-226.		
V.V.	AE	Requa et al., "The piecewise data flow architecture: Architectural concepts" IEEE Transactions on Computers (1983) C-32(5):425-438.		
V.V.	AF	Wang et al., "I-NET mechanism for issuing multiple instructions" Proceedings of Supercomputing" IEEE Computer Society (November 14-18, 1988) Orlando, Florida, pp. 88-95.		

Examiner Signature	V. Vu	Date Considered	12-3-01
--------------------	-------	-----------------	---------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231. PA 3186157 v1